TOSHIBA Field Effect Transistor Silicon P Channel MOS Type

### SSM6P09FU

### **High Speed Switching Applications**

Unit: mm

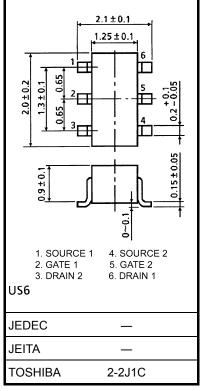
- · Small package
- Low Drain-Source ON resistance.
  - $: R_{on} = 2.7 \Omega \text{ (max) } (@V_{GS} = -10 \text{ V})$
  - $: R_{on} = 4.2 \Omega \text{ (max) } (@V_{GS} = -4 \text{ V})$

## Absolute Maximum Ratings (Ta = 25°C) (Q1, Q2 Common)

Characteristics		Symbol	Rating	Unit	
Drain-Source voltage		$V_{DS}$	-30	V	
Gate-Source voltage		$V_{GSS}$	±20	V	
Drain current	DC	ΙD	-200	mA	
	Pulse	I <sub>DP</sub>	-800		
Drain power dissipation (Ta = 25°C)		P <sub>D</sub> (Note 1)	300	mW	
Channel temperature		T <sub>ch</sub>	150	°C	
Storage temperature range		T <sub>stg</sub>	-55~150	°C	

Storage temperature range T<sub>stg</sub> –55~150 °C

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e.



Weight: 6.8 mg (typ.)

operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1: Total rating, mounted on FR4 board (25.4 mm  $\times$  25.4 mm  $\times$  1.6 t, Cu Pad: 0.32 mm $^2$   $\times$  6) Figure 1.

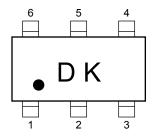
### **Handling Precaution**

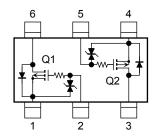
When handling individual devices (which are not yet mounting on a circuit board), be sure that the environment is protected against electrostatic electricity. Operators should wear anti-static clothing, and containers and other objects that come into direct contact with devices should be made of anti-static materials.

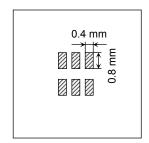
### Marking

# Equivalent Circuit (top view)

Figure 1: 25.4 mm  $\times$  25.4 mm  $\times$  1.6 t, Cu Pad: 0.32 mm<sup>2</sup>  $\times$  6





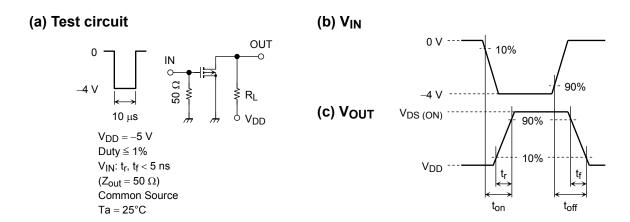


### Electrical Characteristics (Ta = 25°C) (Q1, Q2 common)

Characteristics		Symbol	Test Condition	Min	Тур.	Max	Unit	
Gate leakage current		I <sub>GSS</sub>	$V_{GS} = \pm 16 \text{ V}, V_{DS} = 0$	_	_	±1	μА	
Drain-Source breakdown voltage		V (BR) DSS	$I_D = -1$ mA, $V_{GS} = 0$	-30	_	_	V	
Drain cut-off current		I <sub>DSS</sub>	$V_{DS} = -30 \text{ V}, V_{GS} = 0$	_	_	-1	μА	
Gate threshold voltage		$V_{th}$	$V_{DS} = -5 \text{ V}, I_D = -0.1 \text{ mA}$	-1.1	_	-1.8	V	
Forward transfer admittance		Y <sub>fs</sub>	$V_{DS} = -5 \text{ V}, I_D = -100 \text{ mA} \text{ (Note2)}$	115	_	_	mS	
Drain-Source ON resistance		R <sub>DS</sub> (ON)	$I_D = -100 \text{ mA}, V_{GS} = -10 \text{ V (Note2)}$	_	2.1	2.7	Ω	
			$I_D = -100 \text{ mA}, V_{GS} = -4 \text{ V}$ (Note2)	_	3.3	4.2		
			$I_D = -100 \text{ mA}, V_{GS} = -3.3 \text{ V(Note2)}$	_	4.0	6.0		
Input capacitance		C <sub>iss</sub>	$V_{DS} = -5 \text{ V}, V_{GS} = 0, f = 1 \text{ MHz}$	_	22	_	pF	
Reverse transfer capacitance		C <sub>rss</sub>	$V_{DS} = -5 \text{ V}, V_{GS} = 0, f = 1 \text{ MHz}$	_	5	_	pF	
Output capacitance		Coss	$V_{DS} = -5 \text{ V}, V_{GS} = 0, f = 1 \text{ MHz}$	_	14	_	pF	
Switching time	Turn-on time	t <sub>on</sub>	$V_{DD} = -5 \text{ V}, I_D = -100 \text{ mA},$	_	85	_	- ns	
	Turn-off time	t <sub>off</sub>	V <sub>GS</sub> = 0~-4 V	_	85	_		

Note2: Pulse test

### **Switching Time Test Circuit (Q1, Q2 Common)**

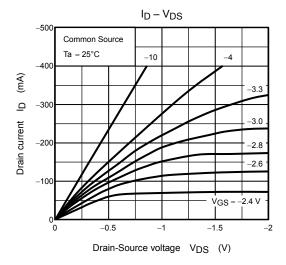


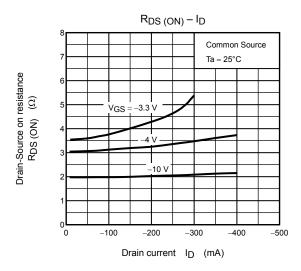
#### **Precaution**

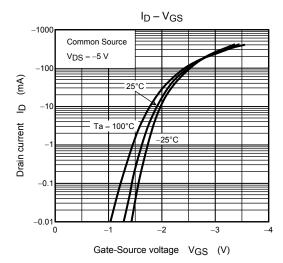
 $V_{th}$  can be expressed as voltage between gate and source when low operating current value is  $I_D = -100~\mu A$  for this product. For normal switching operation,  $V_{GS}$  (on) requires higher voltage than  $V_{th}$  and  $V_{GS}$  (off) requires lower voltage than  $V_{th}$ . (Relationship can be established as follows:  $V_{GS}$  (off)  $< V_{th} < V_{GS}$  (on))

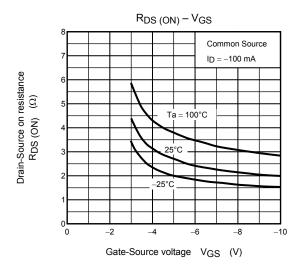
Please take this into consideration for using the device.

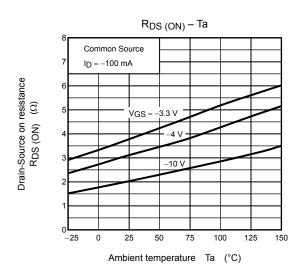
(Q1, Q2 common)

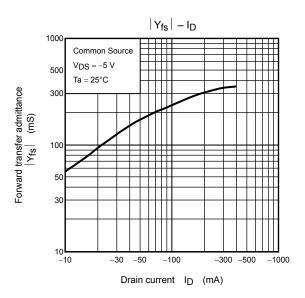






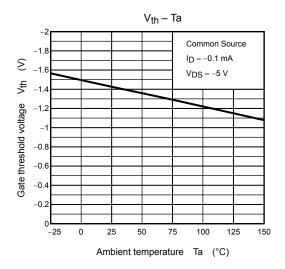


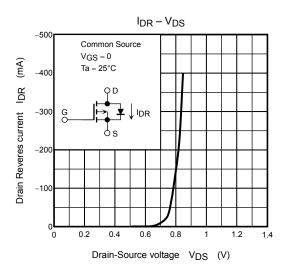


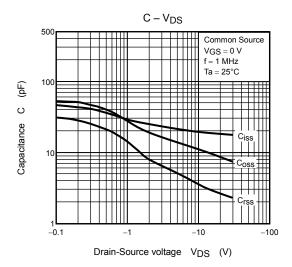


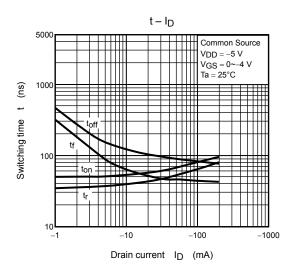
(Q1, Q2 common)

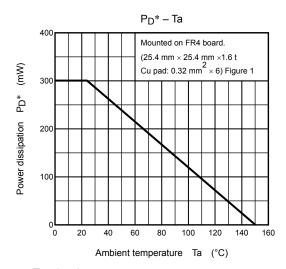
**TOSHIBA** 











\*: Total rating

4

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20070701-EN GENERAL

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